

ABSTRACT

To provide a level shift circuit in which the margin of level shift operation is prevented from deteriorating when the potential difference between a first power supply and a second power supply is large. A level shift circuit for changing the signal level in a first logic circuit fed from a first power supply to the signal level in a second logic circuit fed from a second power supply, comprises: a pull-up and/ or pull-down circuit fed from the second power supply for pulling up and/ or pulling down the output of a level shift core circuit; and a control circuit fed from the second power supply, which receives level shift input signals and level shift output signals for controlling the pull-up and/ or pull-down circuit.